



**राष्ट्रीय प्रौद्योगिकी संस्थान अगरतला**  
National Institute Of Technology, Agartala  
Jirania, Tripura, India, Pin - 799046

F.NITA.12 (78-EE)/IIT Guwahati-TIDF/Project/Padmagirisan/2024/Part-1/JRF Date: 29.08.2024

**Recruitment of Junior Research Fellow (JRF) or Project Assistant (PA) for the research project funded by IIT Guwahati TIDF TIH.**

Applications are invited for the post of **JRF/PA** under the sponsored research project. The details of the project and the post for recruitment are given below:

<b>Title of the Project</b>	:	Development of Wireless Charging System for Underwater Vehicles with Autonomous Docking Capability
<b>Funding Agency</b>	:	IIT Guwahati TIDF TIH
<b>Project duration</b>	:	2 Years (March 2024 to March 2026)
<b>Principal Investigator (PI)</b>	:	Dr. Padmagirisan P, Assistant Professor, Department of Electrical Engineering, NIT Agartala.
<b>Co-PI</b>	:	Dr. Ngangbam Herojit Singh, Assistant Professor, Department of Computer Science & Engineering, NIT Agartala
<b>Emolument per month</b>	:	<b>JRF:</b> Rs. 37,000/- + HRA for first 11 Months and may be extend depending on the project performance (as per applicable rules). (or) <b>PA:</b> Rs. 20,000/- + HRA for first 11 Months and may be extend depending on the project performance (as per applicable rules).
<b>Essential Qualification</b>	:	<b>JRF:</b> Post Graduate Degree in Basic Science OR Graduate/Post Graduate Degree in Professional Course selected through a process described through any one of the following: a. Scholars who are selected through National Eligibility Tests – CSIR-UGC NET including lectureship (Assistant Professorship) and GATE. b. The selection process through National level examinations conducted by central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MoE, ICAR, ICMR, IIT, IISc, IISER, NISER etc. <b>Desirable:</b> M.E./M.Tech. in Electrical Engineering/equivalent with Power Electronics/ Energy Systems/ any other related specialization. (or) <b>PA:</b> B.Sc./3 years Diploma in Engineering & Technology <b>Desirable:</b> B.E./B.Tech. in Electrical Engineering or other related specialization. <b>*Candidates can be considered for the Project Assistant position if no suitable candidates are found for the JRF position.</b>
<b>Description of responsibilities</b>	:	The primary role is to assist in research activities under the guidance of the Principal Investigator (PI) and Co-PI. Research activities include design and development of power electronics hardware including power circuits, gate drivers, signal processing, and embedded controller. Circuit simulations and hardware testing of both components and full converter system. Preparation of high-quality documentation, including detailed reports and presentations etc.

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29/08/24



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<b>Number of Post</b>	:	1 (One) JRF or 1 (One) PA
<b>Date of Interview (Walk-in interview online/offline)</b>	:	<b>20.09.2024</b> (tentative) Mode of interview: Online/Offline depending on the suitability.

**Terms and Conditions:**

The position is purely on temporary basis and will be governed by the Administrative rules, conditions imposed by Research & Consultancy Cell, NIT Agartala.

1. Candidates before appearing for the interview shall ensure that they are eligible for the position they intend to apply.
2. Interested candidate should send their filled application including photograph and signed scan copy of educational qualifications, research publications and a statement of interest on or before **18.09.2024**. The application should be sent to **Dr. Padmagirisan P**, Department of Electrical Engineering, National Institute of Technology (NIT), Agartala, Tripura, India or through e-mail to **ppadmagirisan@nita.ac.in** keeping **tih@iitg.ac.in** in CC.
3. The project duration is 2 years (March 2024 to March 2026) for the position and is co-terminus with the project.
4. No TA/DA is permissible for appearing in the interview and joining in case of selection. Institute reserves the right to cancel the process at any point of time without assigning any reason.
5. Candidate shall bring along with them the original degree(s)/certificate(s) and experience certificate(s) at the time of interview for verification.
6. Appointment will be temporary in nature and as per the Institute terms and conditions.
7. Only shortlisted candidates will be informed through email provided in the application form for interview, upon selection engagement letter will be sent through the email.
8. The applicant will be responsible for the authenticity of information, documents and photographs submitted.
9. The Institute reserves the right to cancel the recruitment without assigning any reason.
10. The candidate selected for the above position will have the option to register for PhD program at Department of Electrical Engineering, National Institute of Technology (NIT) Agartala. However, for admission to PhD program, the candidate must meet certain eligibility requirements as per the Ph.D rules 2018 of NIT Agartala.
11. The temporary engagement can be terminated without assigning any reason.

Sd/

**Dean(R&C)**  
**NIT Agartala**

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## APPLICATION FORM

Recent passport  
size color  
photography  
affixed

Application for the post of .....

Advt. No.: .....

Dated:.....

- 1 Name of the Applicant :
- 2 Father's/Husband's Name :
- 3 Nationality :
- 4 Date of Birth :
- 5 Category (SC/ST/OBC/GEN/EWS) :  
(Attach copy of Certificate)
- 6 Gender (Male/Female) :
- 7 Marital Status :
- 8 NET Qualified (Yes/No) :  
(Attach copy of Certificate)
- 9 GATE Qualified (Yes/No) :  
(Attach Copy of Certificate)
- 10 Title of the project undertaken in :  
M.Tech /M.Sc./B.Tech
- 11 Research Experience (If any) (Attached :  
Certificate)
- 12 Aadhar No. (attach copy of Aadhar :  
Card)
13. Present Address .....
- .....
- .....Pin Code: .....
- E-mail ID: .....Phone/Mobile No.: .....

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14. Educational Qualification (Higher School onwards) (Attached Certificate) :

Exam Passed	Year of passing	Board/ University	Subjects	Percentage of marks

15. List of SCI Journal:

16. List of Scopus Journal:

17. List of Conference Paper:

18. Book/Book Chapter/Patent:

**UNDERTAKING**

I hereby declare that all the information furnished above is true, correct and complete to the best of my knowledge and belief.

Signature of the Candidate

Date: .....

Place: .....

*[Handwritten Signature]*  
29/08/24