



राष्ट्रीय प्रौद्योगिकी संस्थान अगरतला
National Institute Of Technology, Agartala
Jirania, Tripura, India, Pin - 799046

Advt. No.: F.NITA.12 (46-EE)/EMEQ/Padmagirisan/2023/Part-1/JRF

Date: 24.10.2024

Recruitment of Junior Research Fellow (JRF) for the research project funded by Science and Engineering Research Board, Department of Science & Technology, Government of India.

Applications are invited for the post of JRF under the sponsored research project. The details of the project and the post for recruitment are given below:

Title of the Project	:	Design and development of an on-board charger for electric vehicle with bidirectional capability
Funding Agency	:	Science and Engineering Research Board (SERB), Government of India.
Project duration	:	February 2024 to February 2027
Principal Investigator (PI)	:	Dr. Padmagirisan P Assistant Professor, Department of Electrical Engineering, NIT Agartala.
Emolument per month	:	Consolidated amount of Rs. 31000/- + HRA for first two years and Rs. 35000/- + HRA for the third year (as per applicable rules) *Enhanced emoluments may be paid subjected to the approval of the funding agency as per applicable norms.
Essential Qualification	:	Post Graduate Degree in Basic Science OR Graduate/Post Graduate Degree in Professional Course selected through a process described through any one of the following: a. Scholars who are selected through National Eligibility Tests – CSIR-UGC NET including lectureship (Assistant Professorship) and GATE. b. The selection process through National level examinations conducted by central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MoE, ICAR, ICMR, IIT, IISc, IISER, NISER etc. Desirable: M.E./M.Tech. in Electrical Engineering/equivalent with Power Electronics/ Energy Systems/ any other related specialization.
Description of responsibilities	:	The primary role is to assist in research activities under the guidance of the Principal Investigator (PI). Research activities include design and development of power electronics hardware including power circuits, gate drivers, signal processing, and embedded controller. Circuit simulations and hardware testing of both components and full converter system. Preparation of high-quality documentation, including detailed reports and presentations etc.
Number of Post	:	1 (One)
Date of Interview (Walk-in interview online/offline)	:	Date: 12 November 2024, Time: 11.00 A.M. onwards. Venue: Seminar Room, Electrical Engineering, NIT Agartala Mode of interview: Walk-in/Online/Offline (depending on the suitability).

National Institute of Technology Agartala
Barjala, Jirania, West Tripura, Pin:- 799046

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Terms and Conditions:

The position is purely on temporary basis and will be governed by the Administrative rules, conditions imposed by Research & Consultancy Cell, NIT Agartala.

1. Candidates before appearing for the interview shall ensure that they are eligible for the position they intend to apply.
2. Interested candidate should send their filled application (in the attached format) including photograph and signed scan copy of educational qualifications, research publications, other supporting documents and a statement of interest on or before **Interview date and time**. The application should be sent to **Dr. Padmagirisan P**, Department of Electrical Engineering, National Institute of Technology (NIT), Agartala, Tripura, India or through e-mail at **padmagirisan@outlook.com** with subject line "Application for JRF position".
3. The project duration is 3 years (February 2024 to February 2027) for the position and is co-terminus with the project.
4. No TA/DA is permissible for appearing in the interview and joining in case of selection. Institute reserves the right to cancel the process at any point of time without assigning any reason.
5. Candidate shall bring along with them the original degree(s)/certificate(s) and experience certificate(s) at the time of interview for verification.
6. Appointment will be temporary in nature and as per the Institute terms and conditions.
7. Only shortlisted candidates will be informed through email provided in the application form for interview, upon selection engagement letter will be sent through the email.
8. The applicant will be responsible for the authenticity of information, documents and photographs submitted.
9. The Institute reserves the right to cancel the recruitment without assigning any reason.
10. The candidate selected for the above position will have the option to register for PhD program at Department of Electrical Engineering, National Institute of Technology (NIT) Agartala. However, for admission to PhD program, the candidate must meet certain eligibility requirements as per the Ph.D rules 2018 of NIT Agartala.
11. The temporary engagement can be terminated without assigning any reason.

Sd/

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NIT Agartala



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APPLICATION FORM

Application for the post of
Advt. No.: Dated:.....

Recent passport size
color photography
affixed

1. Name of the Applicant :
2. Father's/Husband's Name :
3. Nationality :
4. Date of Birth :
5. Category (SC/ST/OBC/GEN/EWS) :
(attach copy of certificate)
6. Gender (Male/Female) :
7. Marital Status :
8. NET Qualified (Yes/No) :
(If Yes, attach copy of certificate)
9. GATE Qualified (Yes/No) :
(If Yes, attach copy of certificate)
10. Title of the project undertaken in :
M.Tech /M.Sc./B.Tech
11. Research Experience (If any) :
(attach Certificate)
12. Aadhar No. :
(attach copy of Aadhar Card)

13. Present Address

.....

.....Pin Code:

E-mail ID:Phone/Mobile No.:

14. Educational Qualification (Higher School onwards) (Attach copy of Certificates) :

Exam Passed	Year of passing	Board/ University	Subjects	Percentage of marks
10 th Std				
12 th Std				
B.Sc./B.Tech. /Equivalent				
M.Sc./M.Tech. /Equivalent				

15. List of SCI Journal:
(attach copy of first page of journal papers)

16. List of Scopus Journal:
(attach copy of first page of journal papers)

17. List of Conference Paper:
(attach copy of first page of conference papers)

18. Book/Book Chapter/Patent:

UNDERTAKING

I hereby declare that all the information furnished above is true, correct and complete to the best of my knowledge and belief.

Signature of the Candidate

Date:

Place: